

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#### PATENT APPLICATION

Applicant(s): Danek et al. Case: 761C4/CPI/L/B/PJS

Serial No.: 10/716,096 Filed: 11/18/2003

Title: CHAMBER FOR CONSTRUCTING A FILM ON A SEMICONDUCTOR WAFER

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S I R:

#### Disclosure Statement under 37 CFR 1.56 and 1.98

Pursuant to 37 CFR 1.56, the documents listed on the attached form PTO-1449 are disclosed. Pursuant to 37 CFR 1.98(d), these documents were previously cited by or submitted with a Disclosure Statement in a prior application that is relied upon by the above-identified Continuation application for an earlier filing date under 35 U.S.C. 120. As such, the documents may be found in the file wrapper of U.S. patent application serial number 08/808,246, filed on February 28, 1997, now U.S. Patent 6,699,530 issued March 2, 2004; U.S. Patent Application No. 08/680,913, filed on July 12, 1996; U.S. Patent Application No. 08/677,218, filed on July 9, 1996; U.S. Patent Application No. 08/677,185, filed on July 9, 1996, now U.S. Patent 6,155,198 issued December 5, 2000; U.S. Patent Application No. 08/567,461, filed on December 5, 1995; and U.S. Patent Application No. 08/498,990, filed on July 6, 1995. Inclusion of these documents on any patent issuing from this continuation application is respectfully requested. Under rule 37 C.F.R. 1.98(a), the applicant submits that no specific comments are necessary for any of the cited English language publications.

Respectfully submitted

Mar 30,200

By: Keith P. Taboada, Esq.

Req. No. 45,150

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## CERTIFICATE OF MAILING under 37 C.F.R. 1.8(a)

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 5

I	persons are required to respond to a collection of	of information unless it displays a valid OMB control number
	Application Number Filing Date	10/716,096
	First Named Inventor	Danek et al.
	Group Art Unit Examiner Name	
	Attorney Docket Number	761C4/CPI/L/B/PJS
	Submission Date	)

			U.S. PATENT	DOCUMENTS	
Examiner Initials*	Cite Document Number		Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant
	110.	Number-Kind Code <sup>2 (if known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Figures Appear
	A1	US-4,173,661	11-1979	Bourdon	
	A2	US-4,420,498	12-13-1983	Hirose et al.	
	А3	US-4,480,010	10-30-1984	Sasanuma et al.	
	A4	US-4,486,462	12-04-1984	Asahi, et al.	
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	A13	US-4,965,090	10-23-1990	Gartner, et al.	
	A14	US-4,985,372	1-15-1991	Narita	
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	A16	US-5,011,705	04-30-1991	Tanaka	
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Initials*	No.¹	Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
	B1	EP 0 174 743	03-19-1986	Morton Thiokol Inc.		
	B2	EP 0 376 709	07-04-1990	Inoue et al		
	В3	EP 0 446 988	9/18/1991	Eniricerche SpA		
	B4	EP 0 477 990	04/1/1998	Applied Materials		
	B5	EP-0 545 602	6-1993	SGS THOMSON MICROELECTRONICS		

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Substitute	for form 1449A/PTO			Application Number Filing Date	10/716,096 11/18/2003	$\rightarrow$
INFO	RMATION DISC	CLOS	SURE	First Named Inventor	Danek et al.	
STAT	EMENT BY AP	PLIC	CANT	Group Art Unit		
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	B6	EP-0 678 903	10-1995	Tel Yamanishi KK		
	B7	EP 0 711 846	5/15/1996	Applied Materials		
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	B10	JP 59-197131	03-12-1985	Toshiba		

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				Filing Date	11/18/2003	
INFO	RMATION DISC	LOS	SURE	First Named Inventor	Danek et al.	
STAT	<b>TEMENT BY AP</b>	PLIC	CANT	Group Art Unit		
				Examiner Name		
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	B11	JP 63-229814	9/26/1988	NEC		
	B12	JP 02-310918	03-12-1991	Fujitsu		
	B13	JP 03-135018	09-04-1991	Hitachi		
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	B15	JP 05-230,639	12-17-1993	Oki Electric Inc.		

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Substitute	for form 1449A/PTO			Application Number	10/716,096
INFO	RMATION DISC	LOS	SURE	Filing Date First Named Inventor	11/18/2003  Danek et al.
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,	(Use as many sheets as ned	essary)	)	Attorney Docket Number	761C4/CPI/L/B/PJS
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)					
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	B17	GB-2 299 345	10-1996	Hyundai Electronics			
	B18						
	B19						
	B20						

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Substitute	for form 1449B/PTO			Application Number	10/716,096
				Filing Date	11/18/2003
INFO	RMATION DISC	CLOS	SURE	First Named Inventor	Danek et al.
STAT	<b>TEMENT BY AP</b>	PLIC	CANT	Group Art Unit	
				Examiner Name	
	(Use as many sheets as ne	cessary	)	Attorney Docket Number	761C4/CPI/L/B/PJS
Sheet	5	of	5	Submission Date	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²
-	C1	EIZENBERG, "Chemical Vapor Deposition of TiN for ULSI applications:, Mat. Res. Symp Process, 427, 1996, Materials Research Society, p325-335	
	C2	PARK et al., "A Novel A1-Reflow Process Using Surface Modification by the ECR Plasma Treatment and its Application to the 256Mbit DRAM", International Electron Devices Meeting, US, New York, pp 109-112, Dec. 11, 1994	
	C3	RIE, et al., "Deposition of Titanium Nitride Layers by Means of Plasma-CVD," Harterei-Kolloquium 42,, October 5-10, 1986, Wiesbaden	
	C4	SMITH, D., "Thin-Film Deposition Principles and Practice," McGraw-Hill, Inc. Publishers, 1995, pp 423-432, 440-447 and 449-452	
	C5	WANG, et al., "Optimization of PVD Ti/CVD TiN Liner for 0.35 um Tungsten," Plug Technology, Mat. Res. Society Symp. Proc. 427, 1996, Materials Res. Soc. P383-387	
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